In re Patent Application of:
ENRIQUEZ

Serial No. 09/686,247 Filed: OCTOBER 11, 2000

## In the Specification:

Please replace the paragraph bridging pages 10 and 11, beginning on line 17 with the following rewritten paragraph:

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The base of transistor 11 is coupled to a first (polarity reversal) voltage reference Vref<sub>pr</sub>, while the base of transistor 12 is coupled to a polarity reversal current source 19 and to a first port 41 of the controlled switching circuit 40. With the dedicated current supply connection shown, the differential transistor pair 11/12 is continuously enabled and drives the polarity reversing circuit 100 for any of the three functional states, so as to ensure that both tip and ring terminals of the SLIC will be fed with the required polarity. Polarity reversal is defined by the polarity reversal current source [16]19 and the external capacitor 60.

Please replace the paragraph bridging pages 14 and 15, beginning on line 14, with the following rewritten paragraph:



Whenever the polarity reversal function is to be employed, each of the bidirectional switches 44 and 45 of the controlled switching circuit 40 is closed, so as to provide a closed circuit path between switching circuit ports 41 and 43 and thereby place the capacitor 60 in circuit with the base of transistor 12 of the differentially coupled pair of transistors 11 and 12 of the polarity reversal coupling circuit 10. In addition, the controlled unidirectional switches 24 and 34 within the ring trip detection coupling circuit 20 and the line parameter measurement coupling [circit] circuit 30 are closed, so as to provide bias current by-pass paths for the differentially coupled transistors pairs and thereby disable each of coupling circuits 20 and 30. As noted above, the differential transistor pair 21/22 is

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continuously enabled and drives the polarity reversing circuit 100 to ensure that both tip and ring terminals of the SLIC are fed with the required polarity, with switching circuit 40 providing a connection path therethrough to capacitor 60. As a result, the application of a (soft) polarity reversal command will have its direction established by the polarity of the polarity reversal current source [16] 19 supplied to port 41, and be sensed by the polarity reversal circuit 100, as intended.